

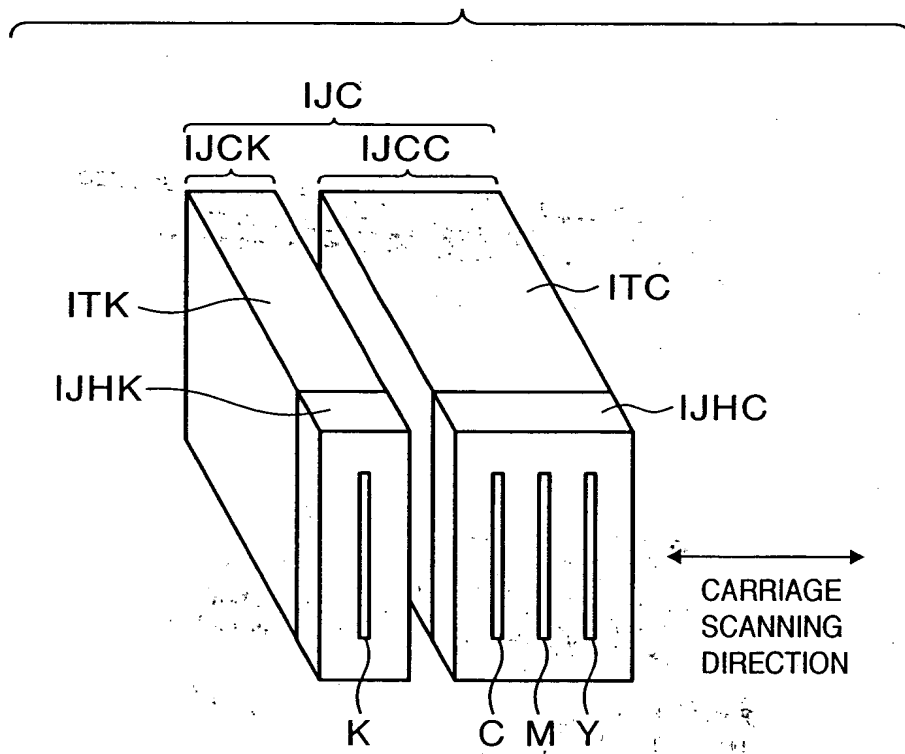
FIG. 2

FIG. 3

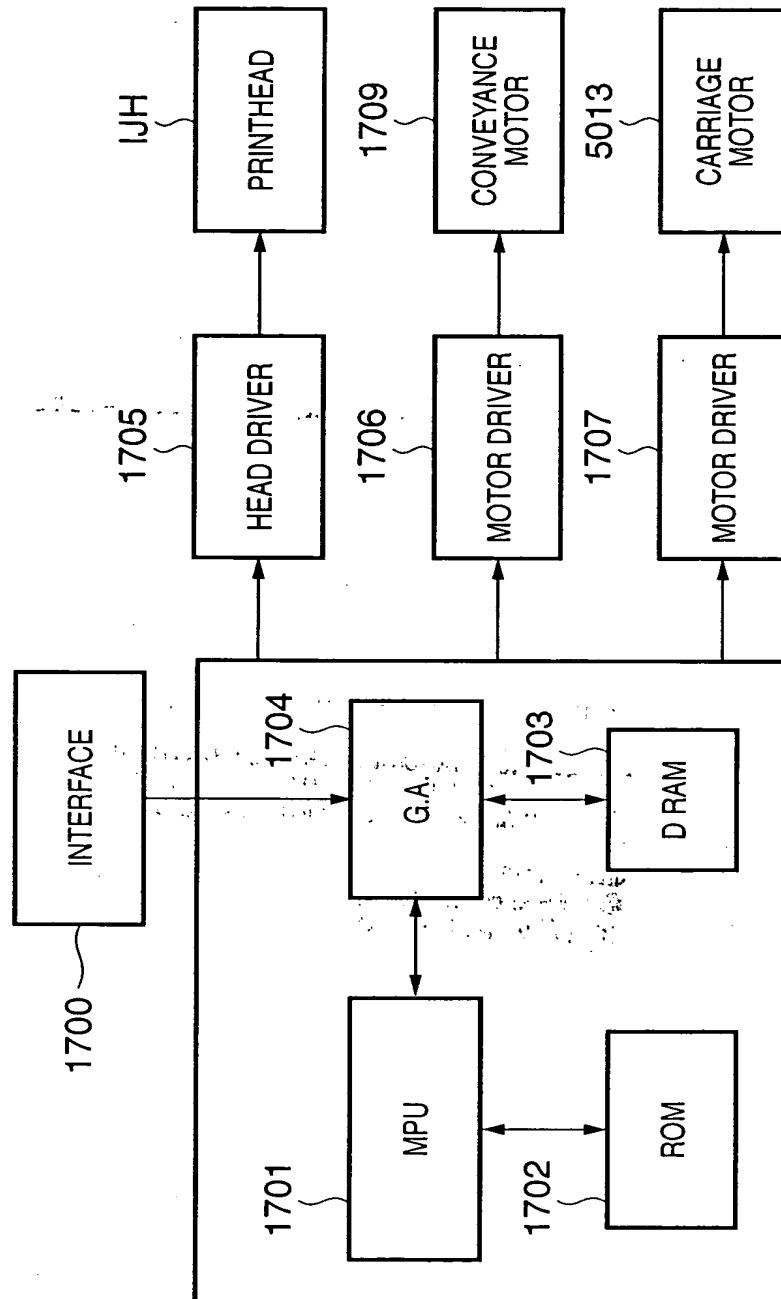


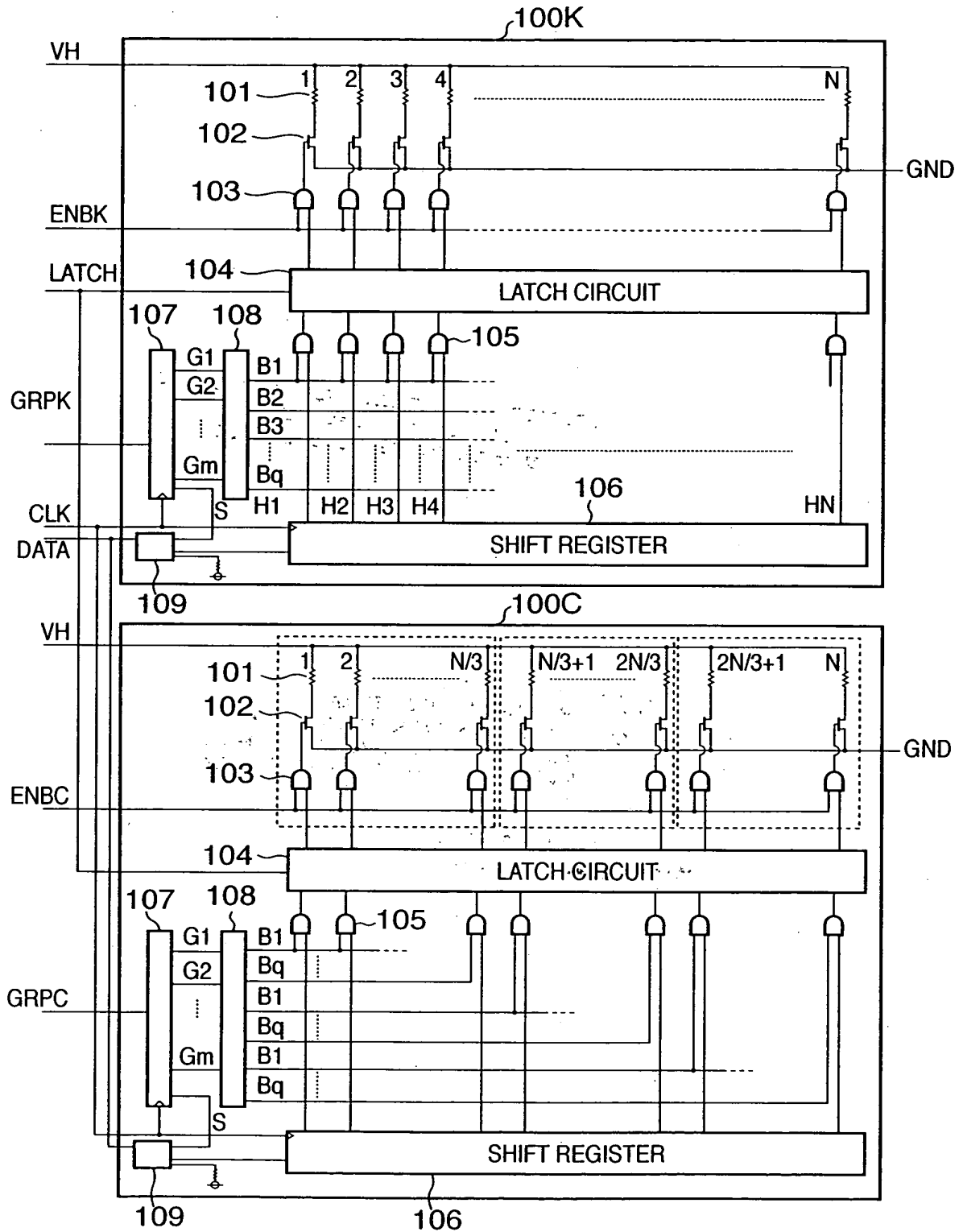
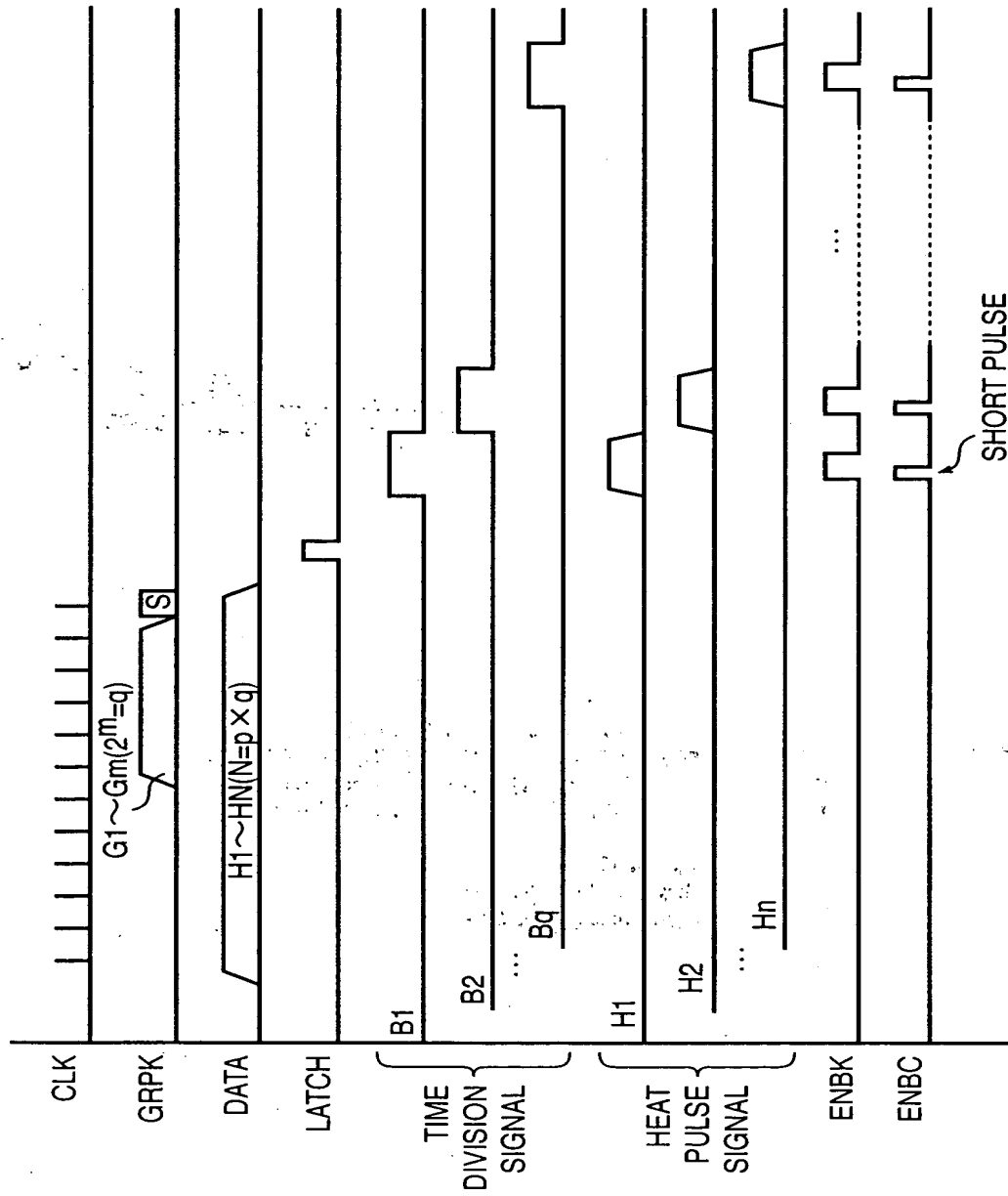
FIG. 4

FIG. 5



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FIG. 6

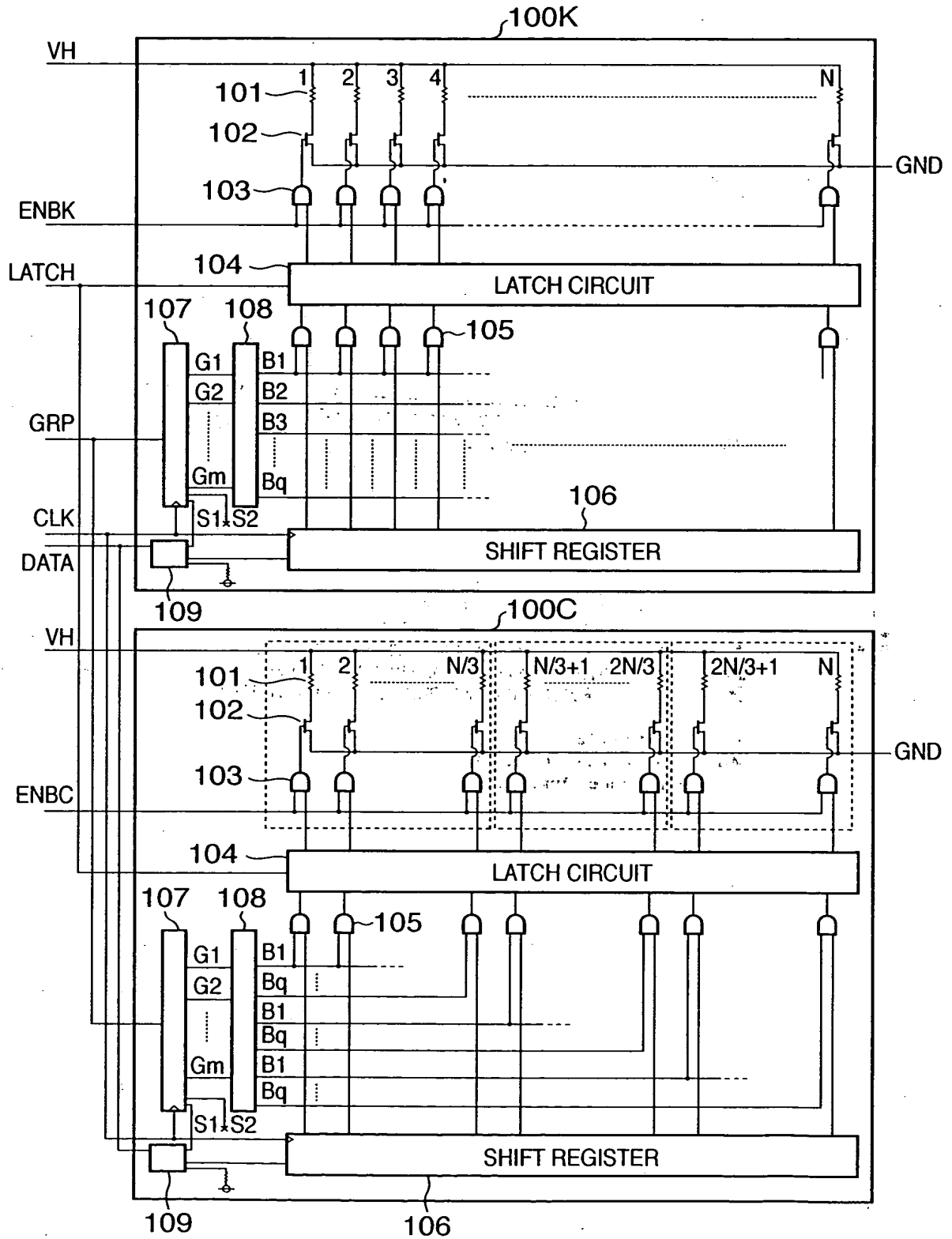
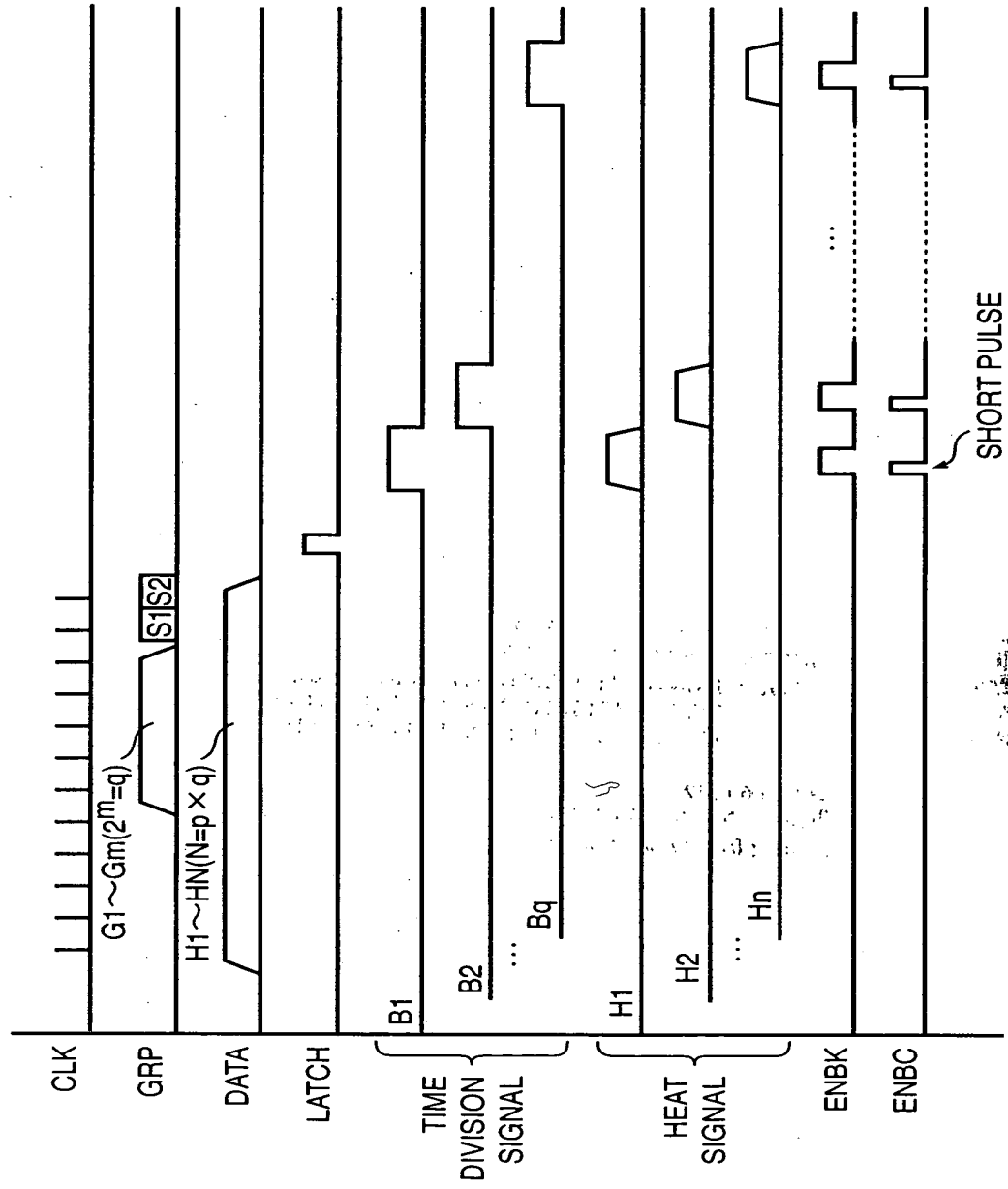


FIG. 7



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FIG. 8

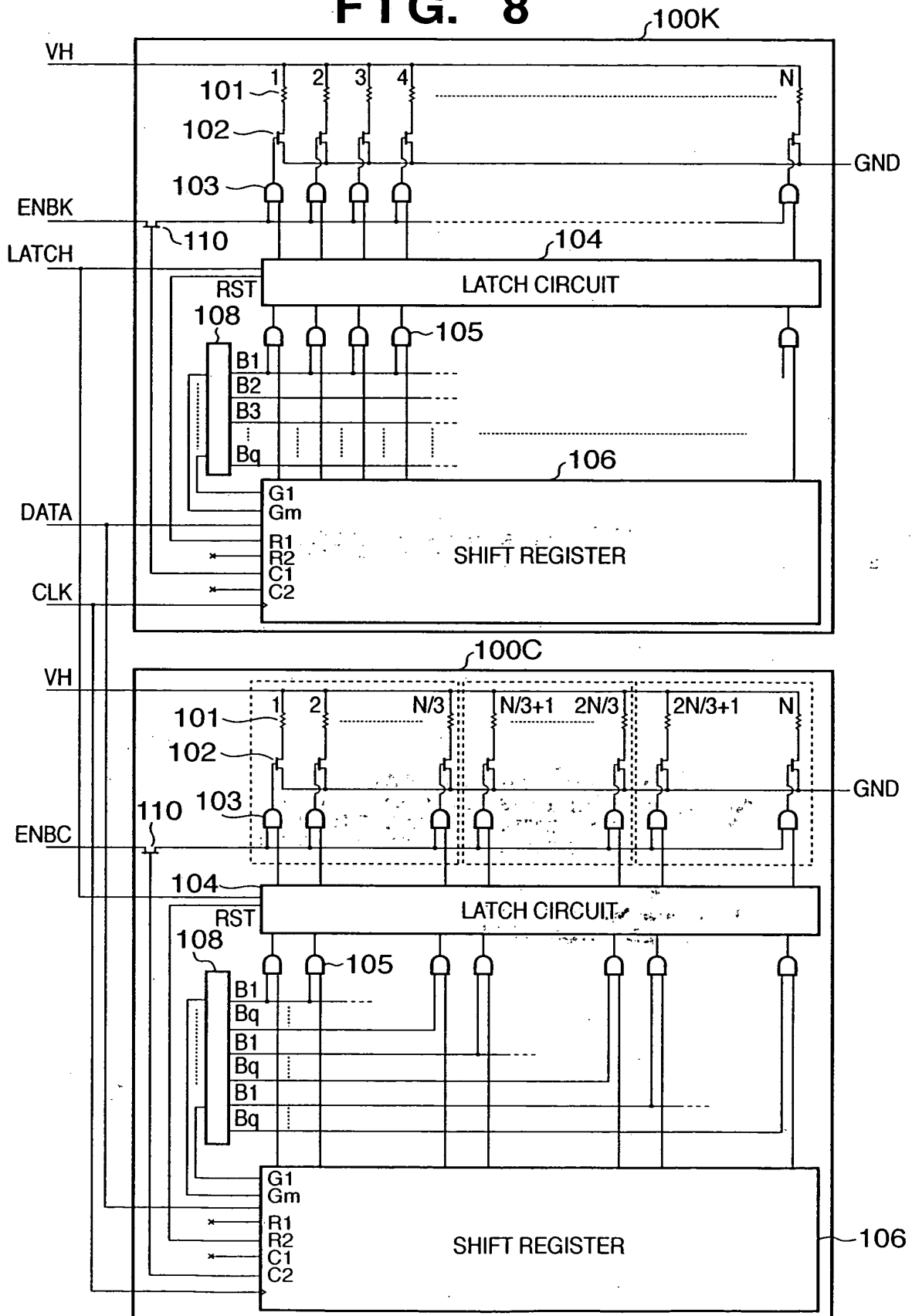


FIG. 9

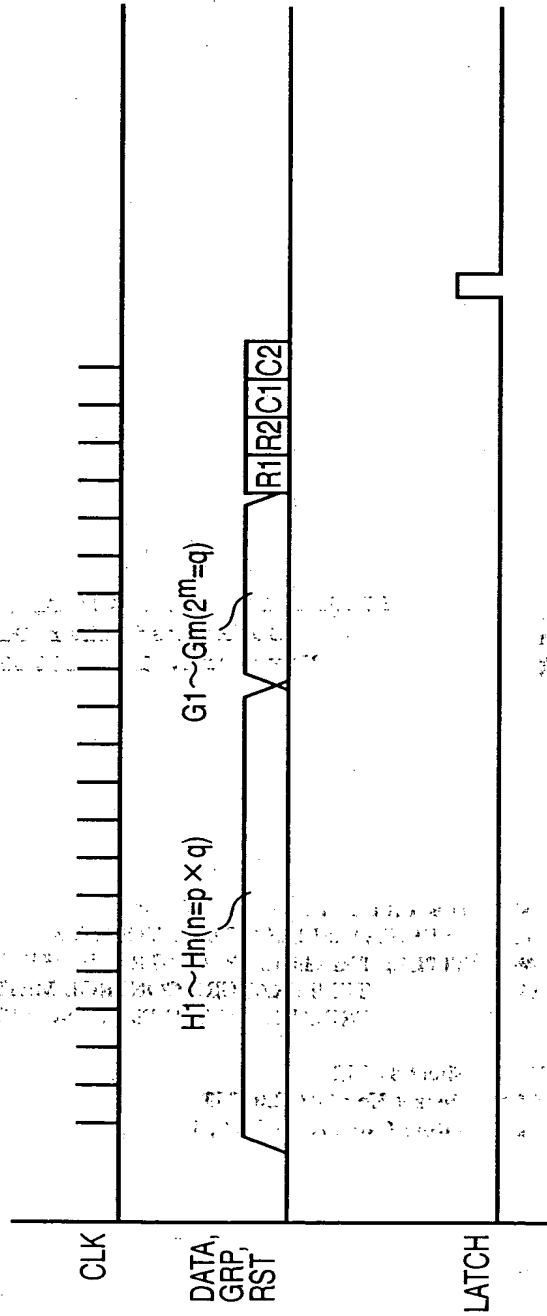


FIG. 10

TRUTH TABLE OF LATCH CIRCUIT

INPUT		OUTPUT
DATA	RESET	
L	L	L
H	L	H
L	H	H
H	H	H

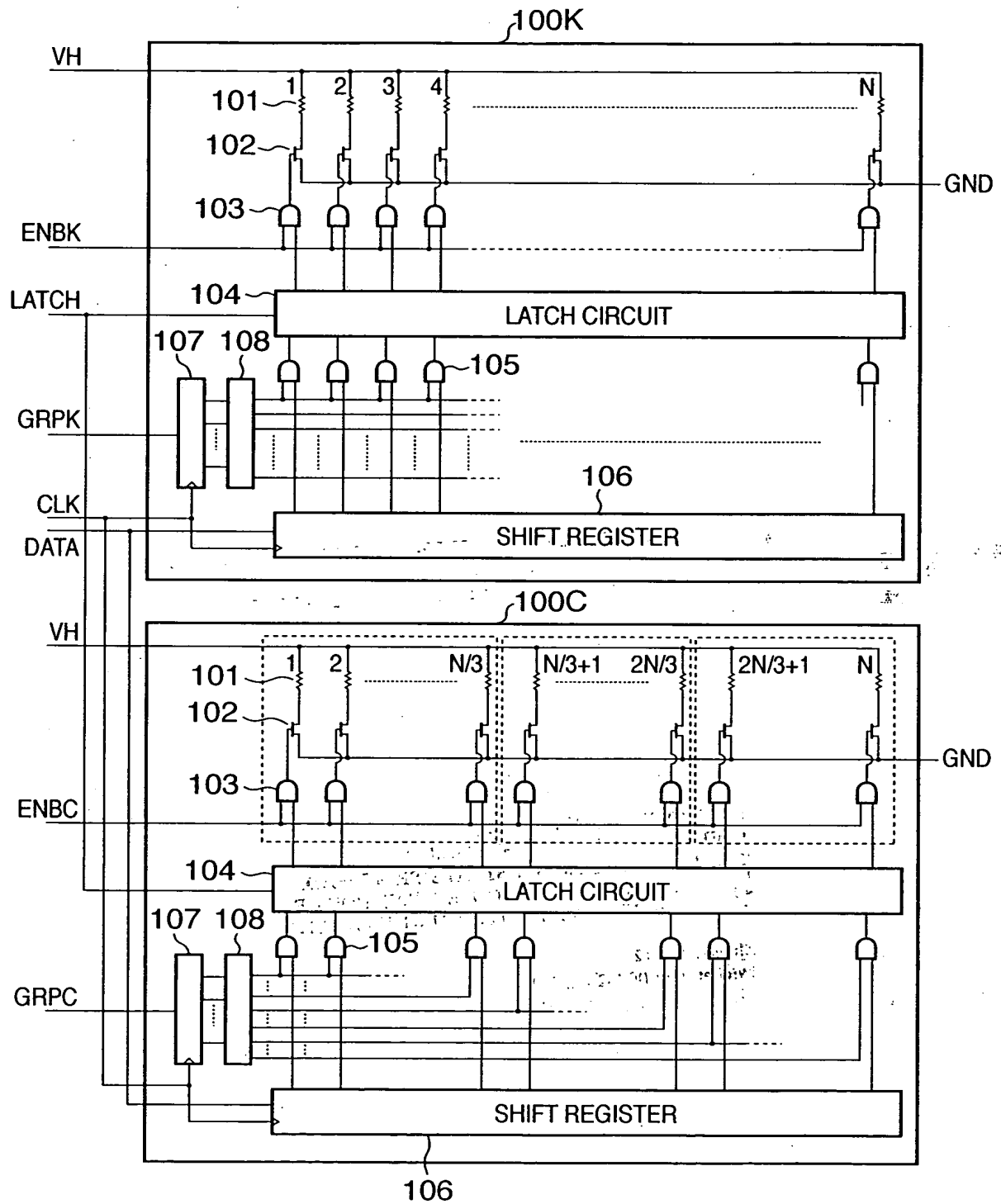
FIG. 11

FIG. 12